



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: James H. Lie  
Serial No.: 09/940,749 Art Unit: 2825  
Filed: August 28, 2001 Examiner: Thompson, A.

For: METHOD AND/OR ARCHITECTURE FOR GENERATING  
SUPERSET PINOUT FOR DEVICES WITH COMBINED  
PROGRAMMABLE LOGIC AND HIGH-SPEED SERIAL CHANNELS

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

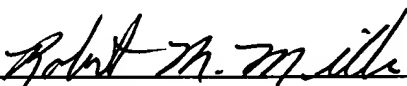
SUBMISSION OF DRAWING AMENDMENT (37 CFR 1.121)

Attached please find

(check applicable items)

- ☒ replacement drawing sheet(s) in permanent ink
- ☐ annotated marked-up copy of the amended drawing(s) with annotations showing the change(s).

CHRISTOPHER P. MAIORANA, P.C.

  
Robert M. Miller  
Registration No. 42,892  
24840 Harper Avenue, Suite 100  
St. Clair Shores, Michigan 48080  
(586) 498-0670

Dated: August 17, 2005

CERTIFICATE OF MAILING (37 CFR 1.8(a))

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail, in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

  
Mary Donna Berkley

Date: August 17, 2005